

Machine Studies

Schedule for Run2-12,2002

August 13th, 0000 - August 13th 0800

Time	Descriptions	Studiers	SRStatus
Tuesday, 07/30/00			
0000-0200	Test short history with pinger in CW mode.	C. Wang	Stored beam and injection
0200-0600	Further optimization of lower emittance lattice. Recover low emittance before end.	Emery Sajaev	Stored beam and injection
0600-0630	Replace the iochdg1 cpu	Arnold	Stored beam and no injection
0600-0700	Test SR corrector FORS-2 chip for bumpless reboot of IOC and PSCU with beam. No beam for first 30 minutes for installation. Uninstall at 1200h.	Fors	Stored beam and injection
0700	Fill SR to 102 mA, low emittance.	OPS	Stored beam and injection
0700-1000	PAR RF function generator setup.	Dimonte T. Smith	Stored beam and no injection
0700-1000	ID xbpm gain vs gap measurements. Note injector not available.	Hahne Roberts	Stored beam and no injection
0700-830	Investigate booster power monitor anomalous trip.	Grelick	Stored beam and no injection

Time	Descriptions	Studiars	SRStatus
0830-1000	Correct booster low emittanace ramp tables.	Sereno	Stored beam and no injection
1000-1300	Optimize PAR RF function generator settings.	Borland Sereno	Stored beam and injection
1000-1200	Further tests of sddscontrollaw in ioc.	Lenkszus Soliday	Stored beam and injection
1200-1300	Hook up a power monitor on TPP-R3. This will take down the booster correctors and HET power supplies.	ES group	No stored beam and no injection
1200-1300	Uninstall FORS-2 chip.	Fors	No stored beam and no injection
1200-1300	Move test equipment on 10-ID front-end EPS.	Behrndt	No stored beam and no injection
1300-1500	Vector mode orbit correction script testing.	Shang Emery	Stored beam and injection
1300-1500	Further CPU correction optimization. Parasitic to above.	Vasserman	Stored beam and injection
1500-1600	Prepare for top-up low-emittance operations	OPS	Stored beam and injection